

Substitute Form PTO-1449  
(Modified)U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10559-303US1Application No.  
10/069,306**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

(37 C.F.R. § 1.98(b))

Applicant  
Gilbert Wolrich et al.Filing Date  
July 3, 2002

Group Art Unit

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
<i>h</i>	AA	09/473,571	12/28/1999	Wolrich et al.			
<i>h</i>	AB	09/475,614	12/30/1999	Wolrich et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
<i>h</i>	AI	WO 01/16697	03/2001	WIPO				
<i>h</i>	AJ	WO 01/16698	03/2001	WIPO				
<i>h</i>	AK	WO 01/16702	03/2001	WIPO				
<i>h</i>	AL	WO 01/16713	03/2001	WIPO				
<i>h</i>	AM	WO 01/16714	03/2001	WIPO				
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<i>h</i>	AQ	WO 01/16758	03/2001	WIPO				
<i>h</i>	AR	WO 01/18646	03/2001	WIPO				

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AS	
	AT	
	AU	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-303US1	Application No. 10/069,306
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.902(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 21, 2002	Group Art Unit

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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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L	AB	WO 01/50247	07/12/2001	WIPO				
L	AC	WO 01/48619	07/05/2001	WIPO				
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L	AI	WO 01/16770	03/08/2001	WIPO				
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L	AK	WO 01/16718	03/08/2001	WIPO				
L	AL	WO 01/15718	03/08/2001	WIPO				
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L	AP	EP 0 745 933	12/04/1996	Europe				
L	AQ	EP 0 633 678	01/11/1995	Europe				
L	AR	EP 0 464 715	01/08/1992	Europe				
L	AS	EP 0 379 709	08/01/1990	Europe				
L	AT	59111533	06/27/1984	Japan				

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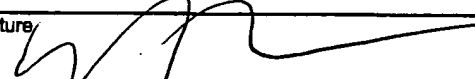
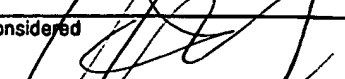
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L	AU	Agarwal et al., "April: A Processor Architecture for Multiprocessing," Proceedings of the 17 <sup>th</sup> Annual International Symposium on Computer Architecture, IEEE, pp. 104-114.
L	AV	Byrd et al., "Multithread Processor Architectures," <i>IEEE Spectrum</i> , Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
L	AW	Chang et al., "A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

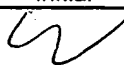
Substitute Form PTO-1449 (Rev. 12-1997)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-303US1	Application No. 10/069,306
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date February 21, 2002	Group Art Unit

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ay	AX	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 <sup>nd</sup> ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
ay	AY	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
ay	AZ	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, 1995, pp. 146-156.
ay	AAA	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," <i>Journal of Parallel and Distributed Computing</i> , Academic Press, Duluth, Minnesota, USA, vol. 40, no. 1, 10 January 1997, pp. 103-117.
ay	ABB	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," IEEE Symposium on FPGAs for Custom Computing Machines, 1998.
ay	ACC	Hauser et al., "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings of the 5 <sup>th</sup> Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1997.
ay	ADD	Hennessy et al., "Computer Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, pp. 476-482 (1998).
ay	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
ay	AFF	Intel, "1A-64 Application Developer's Architecture Guide," Rev.1.0, May 1999, pp. 2-2, 4-29 to 4-31, 7-116 to 7-118 and c-21.
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ay	AHH	Litch et al., "StrongARMing Portable Communications," IEEE Micro, 1998, pp. 48-55.
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ay	AMM	Tremblay et al., "A Three Dimensional Register File for Superscalar Processors," IEEE Proceedings of the 28 <sup>th</sup> Annual Hawaii International Conference on System Sciences, 1995, pp. 191-201.
ay	ANN	Trimberger et al., "A time-multiplexed FPGA," Proceedings of the 5 <sup>th</sup> Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1998.
ay	AOO	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
ay	APP	Vibhatavani et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-359.
ay	AQQ	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
ay	ARR	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20 <sup>th</sup> Annual International Symposium on Computer Architecture, 1993, pp. 120-130.

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	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
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	AL							
	AM							
	AN							
	AO							
	AP							

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	AR	
	AS	
	AT	

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# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

12/02/04

## Title of Invention

## INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR

Application Number : 10/069306



Confirmation Number: 1999

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-303US1

Art Unit:

Examiner:

Search string: ( 6223238 or 6223279 or 6247025 or 6256713 or 6272616 or 6275505 or 6279113 or 6289011 or 6298370 or 6307789 or 6324624 or 6345334 or 6347344 or 6351808 or 6356962 or 6373848 or 6360262 or 6389449 or 6393483 or 6415338 or 6426940 or 6427196 or 6430626 or 6434145 or 6463072 or 6532509 or 6552826 or 6560667 or 6577542 or 6584522 or 6587906 or 6606704 or 6625654 or 6629237 or 6631430 or 6631462 or 6661794 or 6667920 or 6668317 or 6681300 or 6694380 or 4023023 or 5113516 or 5363448 or 5436626 or 5600812 or 5652583 or 5717760 ).pn

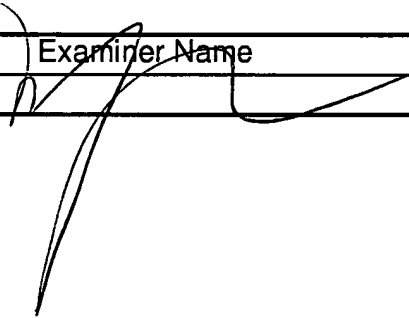
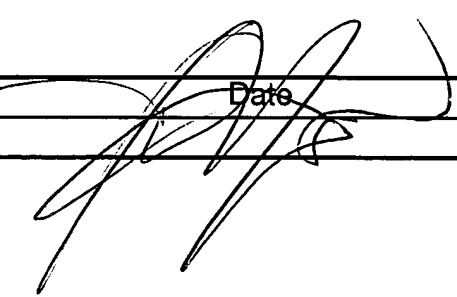
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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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✓	3	6247025	2001-06-12	Bacon			
✓	4	6256713	2001-07-03	Audityan et al.			
✓	5	6272616	2001-08-07	Fernando et al.			
✓	6	6275505	2001-08-14	O Loughlin et al.			
✓	7	6279113	2001-08-21	Vaidya			
✓	8	6289011	2001-09-11	Seo et al.			
✓	9	6298370	2001-10-02	Tang et al.			
✓	10	6307789	2001-10-23	Wolrich et al.			
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Signature

Examiner Name	Date
	

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR
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Application Number : 10/069306  
Confirmation Number: 1999  
First Named Applicant: Gilbert Wolrich  
Attorney Docket Number: 10559-303US1



Art Unit:

Examiner:

Search string: ( 5937187 or 5938736 or 5940612 or 5940866 or 5946487 or 5948081 or 5951679 or 5958031 or 5961628 or 5970013 or 5978838 or 5983274 or 5996068 or 6002881 or 6009515 or 6012151 or 6014729 or 6023742 or 6029228 or 6058168 or 6058465 or 6067585 or 6070231 or 6072781 or 6073215 or 6076158 or 6079008 or 6079014 or 6085215 or 6085294 or 6092127 or 6092158 or 6092175 or 6112016 or 6115811 or 6134665 or 6141689 or 6141765 or 6144669 or 6145054 or 6157955 or 6160562 or 6182177 or 6195676 or 6199133 or 6201807 or 6212542 or 6212611 or 6216220 or 6223207 ).pn

## US Patent Documents

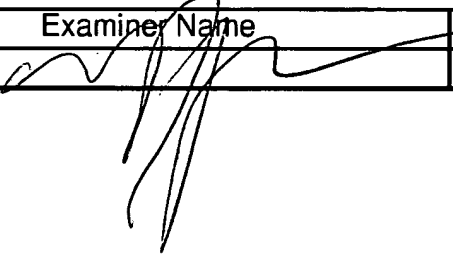
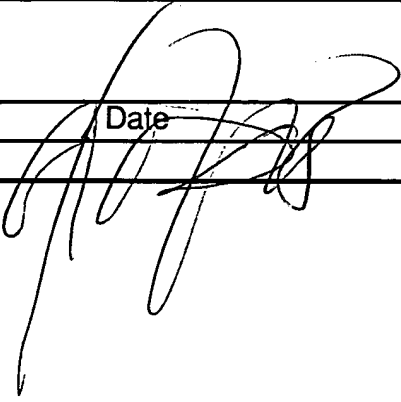
**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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✓	48	6212611	2001-04-03	Nizar et al.			
✓	49	6216220	2001-04-10	Hwang			
✓	50	6223207	2001-04-24	Lucovsky et al.			

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Search string: ( 5568617 or 5574922 or 5574939 or 5592622 or 5606676 or 5613071 or 5613136 or 5623489 or 5627829 or 5630130 or 5644623 or 5649157 or 5659722 or 5680641 or 5689566 or 5692167 or 5699537 or 5717898 or 5721870 or 5742587 or 5742782 or 5742822 or 5745913 or 5751987 or 5761507 or 5761522 or 5781774 or 5784649 or 5784712 or 5796413 or 5797043 or 5809235 or 5809530 or 5812839 or 5812868 or 5828746 or 5828863 or 5832215 or 5835755 or 5854922 or 5860158 or 5886992 or 5887134 or 5890208 or 5892979 or 5905876 or 5905889 or 5915123 or 5933627 or 5659687 ).pn

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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*[Handwritten signatures and initials]*

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# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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## Title of Invention

## INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR

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